



## Description

### JMT N And P-Channel Enhancement Mode MOSFET

#### Features

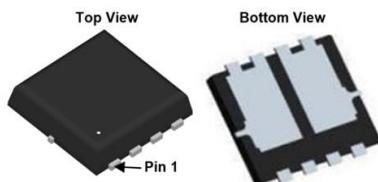
- N-Channel: 30V, 9A
  - $R_{DS(ON)} < 21m\Omega$  @  $V_{GS} = 10V$
  - $R_{DS(ON)} < 35m\Omega$  @  $V_{GS} = 4.5V$
- P-Channel: -30V, -5A
  - $R_{DS(ON)} < 52m\Omega$  @  $V_{GS} = -10V$
  - $R_{DS(ON)} < 90m\Omega$  @  $V_{GS} = -4.5V$
- Excellent Gate Charge x  $R_{DS(ON)}$  Product(FOM)
- Very Low On-resistance  $R_{DS(ON)}$
- Fast Switching Speed

#### Application

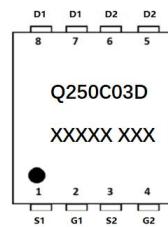
- Battery Protection
- Load Switch
- Power Management



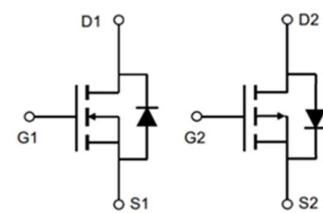
100% UIS TESTED!  
100%  $\Delta V_{ds}$  TESTED!



PDFN3x3-8L-D



Marking and pin Assignment



Schematic Diagram

## Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
Q250C03D	JMTQ250C03D	TAPING	PDFN3x3-8L-D	13inch	5000	50000

## Absolute Maximum Ratings ( $T_C=25^\circ C$ unless otherwise specified)

Symbol	Parameter		Max. N-Channel	Max. P-Channel	Units
$V_{DSS}$	Drain-Source Voltage		30	-30	V
$V_{GSS}$	Gate-Source Voltage		$\pm 20$	$\pm 20$	V
$I_D$	Continuous Drain Current	$T_C = 25^\circ C$	9	-5	A
		$T_C = 100^\circ C$	5.9	-3.3	A
$I_{DM}$	Pulsed Drain Current <sup>note1</sup>		36	-20	A
$E_{AS}$	Single Pulsed Avalanche Energy <sup>note2</sup>		12	12	mJ
$P_D$	Power Dissipation	$T_C = 25^\circ C$	2.9	2.2	W
$R_{eJC}$	Thermal Resistance, Junction to Case		43	57	$^\circ C/W$
$T_J, T_{STG}$	Operating and Storage Temperature Range		-55 to +150		°C

**N-Channel Electrical Characteristics** ( $T_J=25^\circ\text{C}$  unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristics</b>						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu\text{A}$	30	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=30V, V_{GS}=0V$	-	-	1	$\mu\text{A}$
$I_{GSS}$	Gate to Body Leakage Current	$V_{DS}=0V, V_{GS}=\pm 20V$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.0	1.5	2.5	V
$R_{DS(\text{on})}$ note2	Static Drain-Source on-Resistance	$V_{GS}=10V, I_D=5A$	-	16	21	$\text{m}\Omega$
		$V_{GS}=4.5V, I_D=3A$	-	25	35	$\text{m}\Omega$
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=15V, V_{GS}=0V, f=1.0\text{MHz}$	-	490	-	pF
$C_{oss}$	Output Capacitance		-	79	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	61	-	pF
$Q_g$	Total Gate Charge	$V_{DS}=15V, I_D=5.8A, V_{GS}=10V$	-	5.2	-	nC
$Q_{gs}$	Gate-Source Charge		-	0.9	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge		-	1.3	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=15V, I_D=3A, V_{GS}=10V, R_{REN}=3\Omega$	-	4.5	-	ns
$t_r$	Turn-on Rise Time		-	2.5	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	14.5	-	ns
$t_f$	Turn-off Fall Time		-	3.5	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_S$	Maximum Continuous Drain to Source Diode Forward Current	-	-	9	A	
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current	-	-	36	A	
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS}=0V, I_S=9A$	-	0.8	1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition :  $T_J=25^\circ\text{C}, V_{DD}=15V, V_{GS}=10V, L=0.5\text{mH}, R_g=25\Omega, I_{AS}=7A$

$T_J=25^\circ\text{C}, V_{DD}=-15V, V_{GS}=-10V, L=0.5\text{mH}, R_g=25\Omega, I_{AS}=-7A$

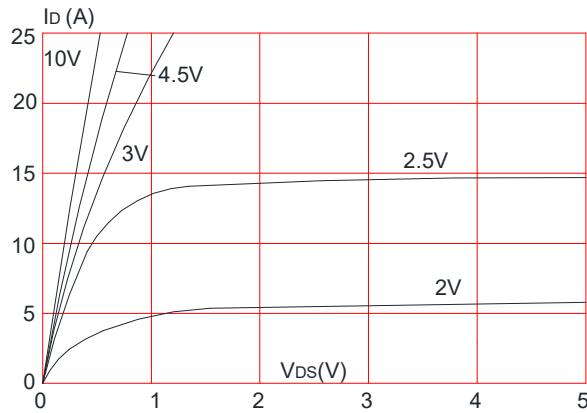
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$ , Duty Cycle $\leq 0.5\%$

**P-Channel Electrical Characteristics** ( $T_J=25^\circ\text{C}$  unless otherwise specified)

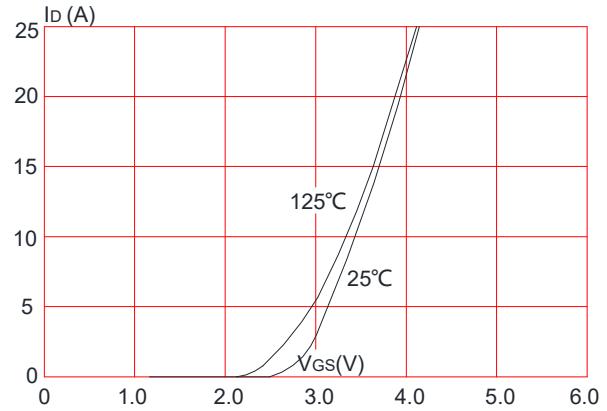
Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristics</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D= -250\mu\text{A}$	-30	-	-	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{DS}= -30\text{V}$ , $V_{GS}=0\text{V}$ ,	-	-	-1	$\mu\text{A}$
$I_{GSS}$	Gate to Body Leakage Current	$V_{DS}=0\text{V}$ , $V_{GS}= \pm 20\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_D= -250\mu\text{A}$	-1.0	-1.6	-2.5	V
$R_{DS(\text{on})}$ note3	Static Drain-Source on-Resistance	$V_{GS}= -10\text{V}$ , $I_D= -4.1\text{A}$	-	40	52	$\text{m}\Omega$
		$V_{GS}= -4.5\text{V}$ , $I_D= -3.5\text{A}$	-	64	90	
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}= -15\text{V}$ , $V_{GS}=0\text{V}$ , $f=1.0\text{MHz}$	-	580	-	pF
$C_{oss}$	Output Capacitance		-	98	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	74	-	pF
$Q_g$	Total Gate Charge	$V_{DS}= -15\text{V}$ , $I_D= -4.1\text{A}$ , $V_{GS}= -10\text{V}$	-	6.8	-	nC
$Q_{gs}$	Gate-Source Charge		-	1	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge		-	1.4	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD}= -15\text{V}$ , $I_D= -1\text{A}$ , $V_{GS}= -10\text{V}$ , $R_{\text{GEN}}=2.5\Omega$	-	14	-	ns
$t_r$	Turn-on Rise Time		-	61	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	19	-	ns
$t_f$	Turn-off Fall Time		-	10	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_s$	Maximum Continuous Drain to Source Diode Forward Current	-	-	-5	A	
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current	-	-	-20	A	
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_s= -5\text{A}$	-	-0.8	-1.2	V

## Typical Performance Characteristics-N

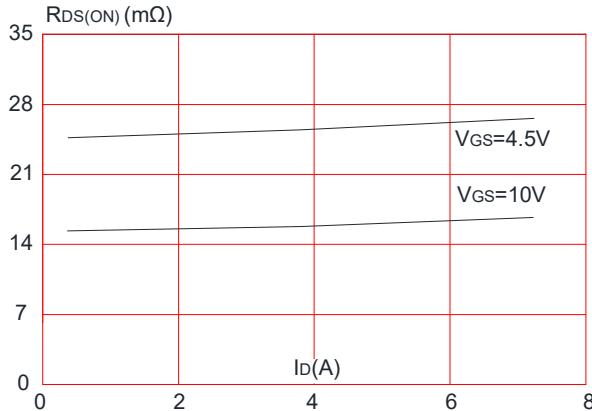
**Figure1:** Output Characteristics



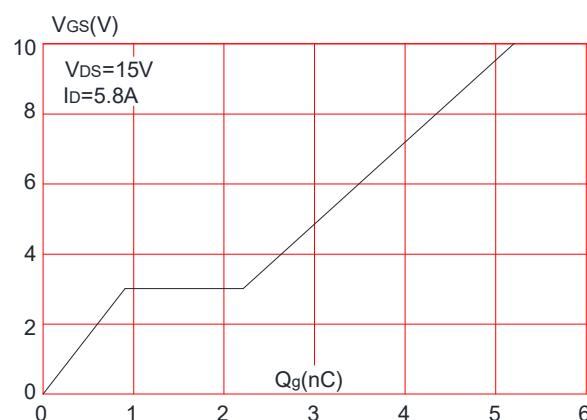
**Figure 2:** Typical Transfer Characteristics



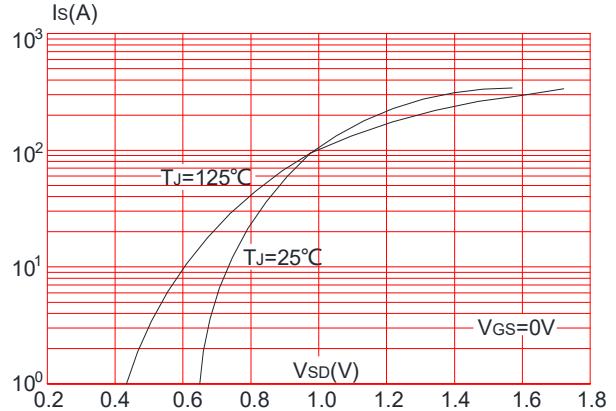
**Figure 3:** On-resistance vs. Drain Current



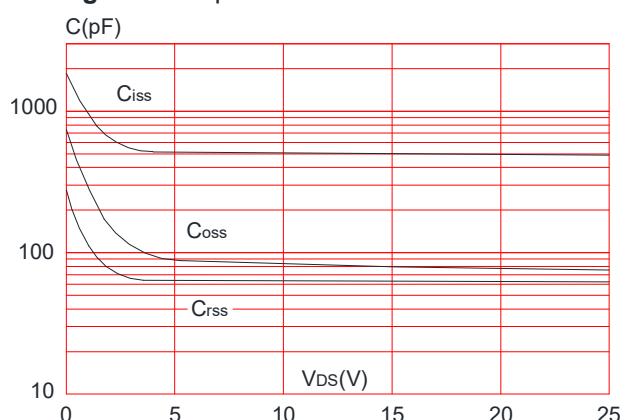
**Figure 5:** Gate Charge Characteristics

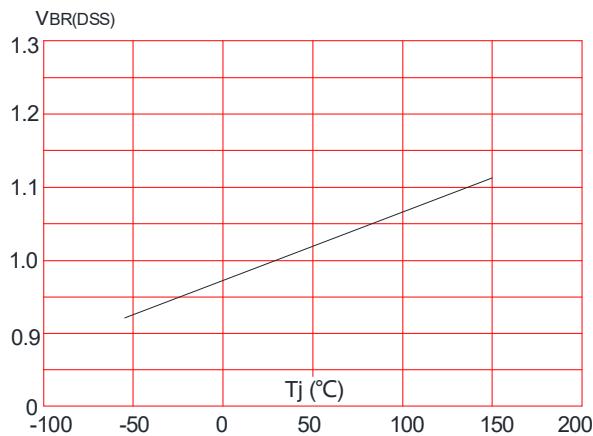


**Figure 4:** Body Diode Characteristics

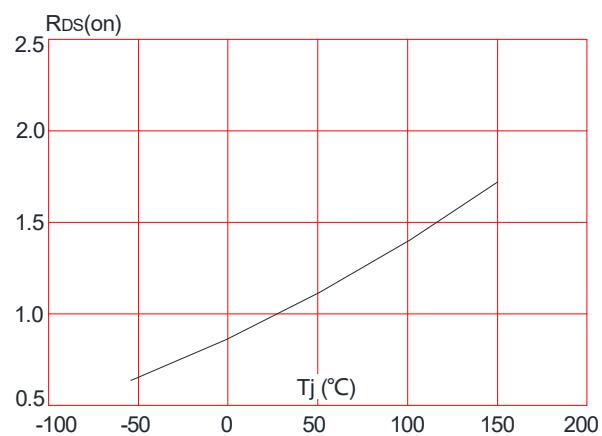


**Figure 6:** Capacitance Characteristics

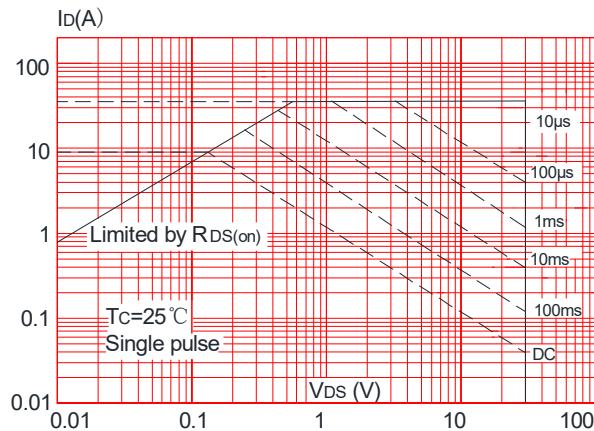




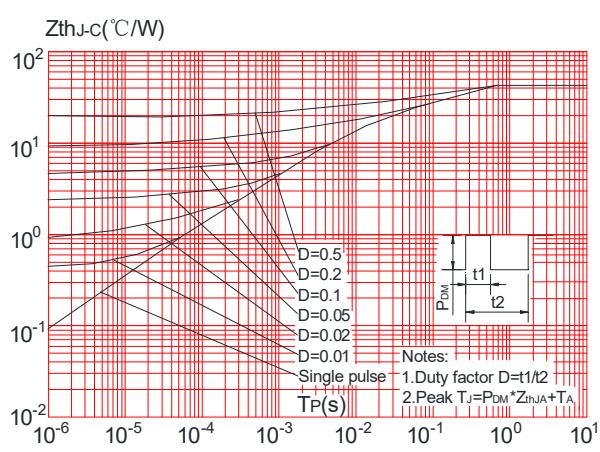
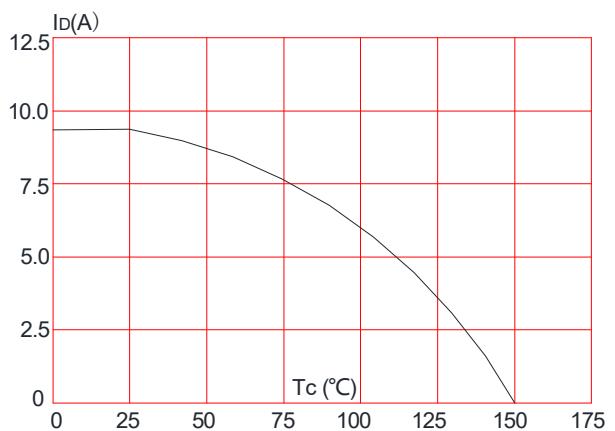
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature



**Figure 11:** Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



## Test Circuit-N

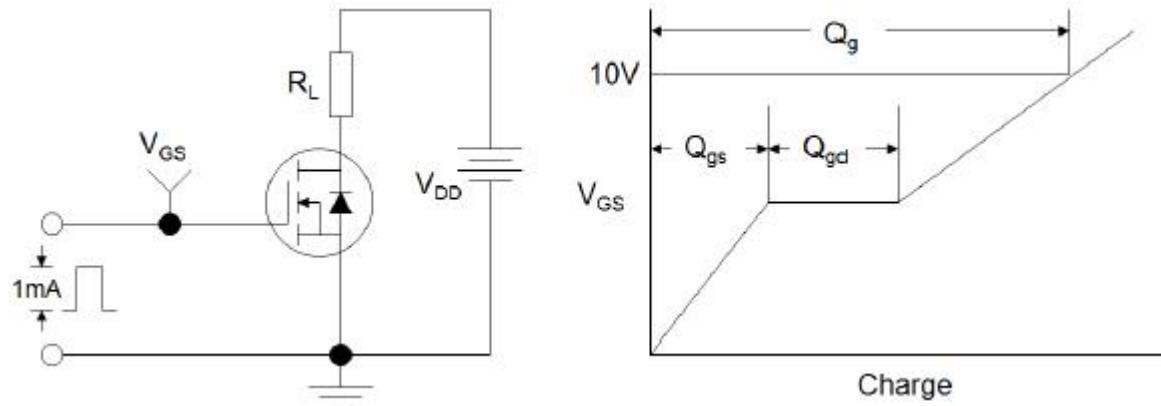


Figure1:Gate Charge Test Circuit & Waveform

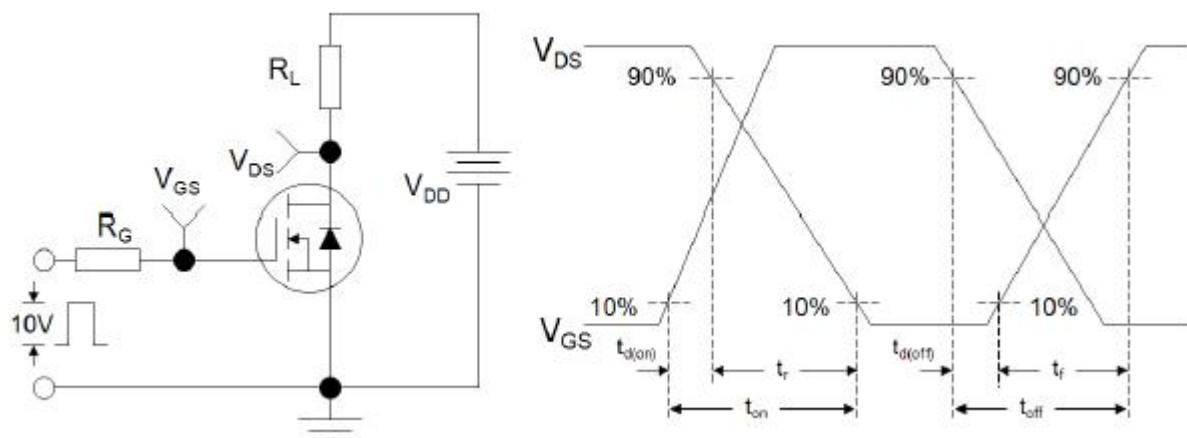


Figure 2: Resistive Switching Test Circuit & Waveforms

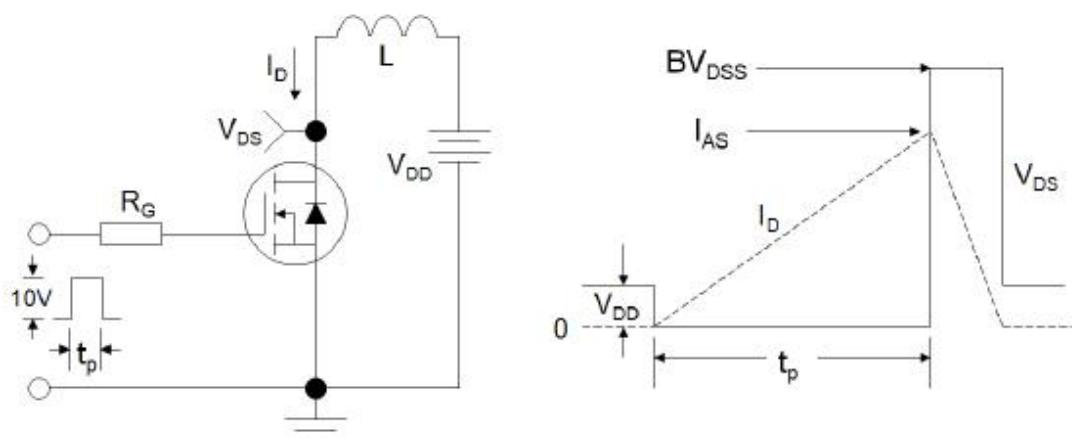
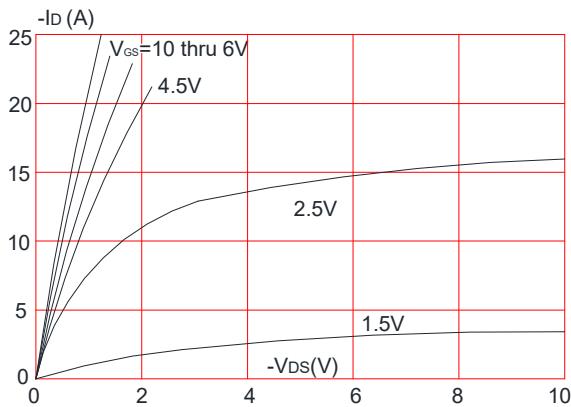


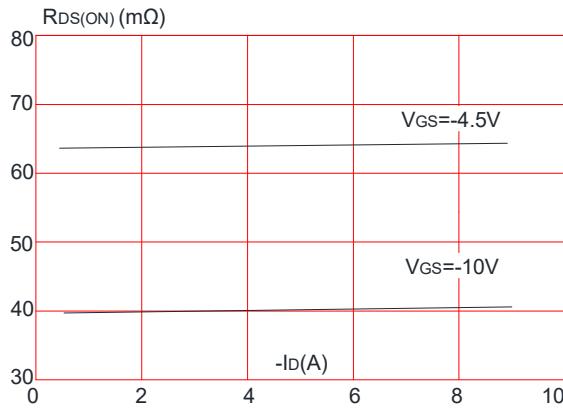
Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms

## Typical Performance Characteristics-P

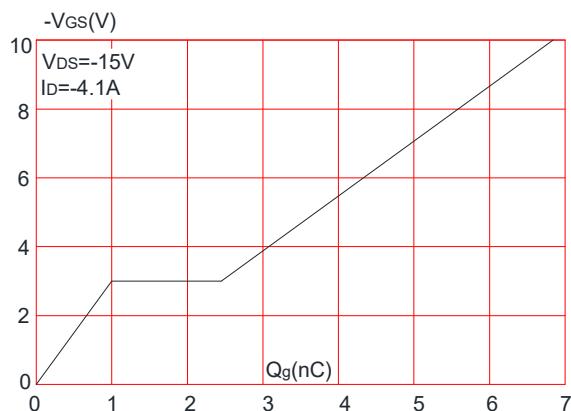
**Figure1:** Output Characteristics



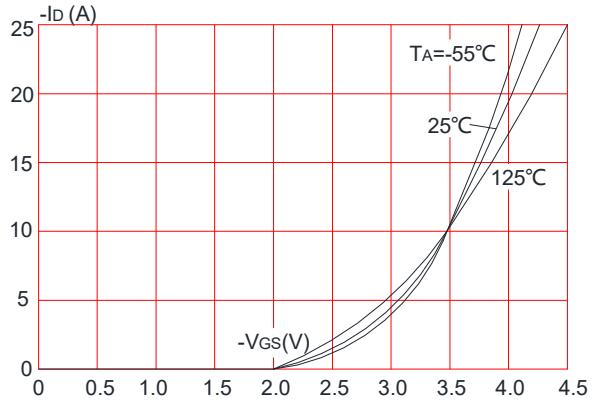
**Figure 3:** On-resistance vs. Drain Current



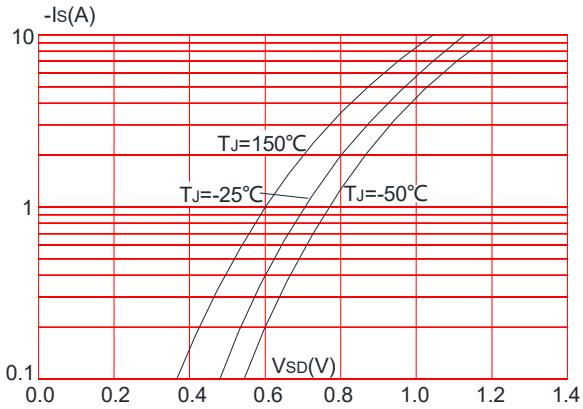
**Figure 5:** Gate Charge Characteristics



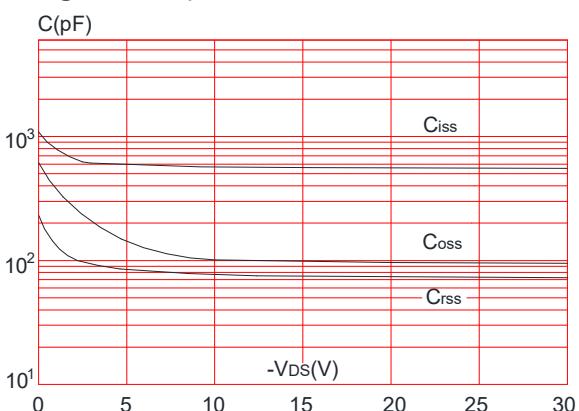
**Figure 2:** Typical Transfer Characteristics



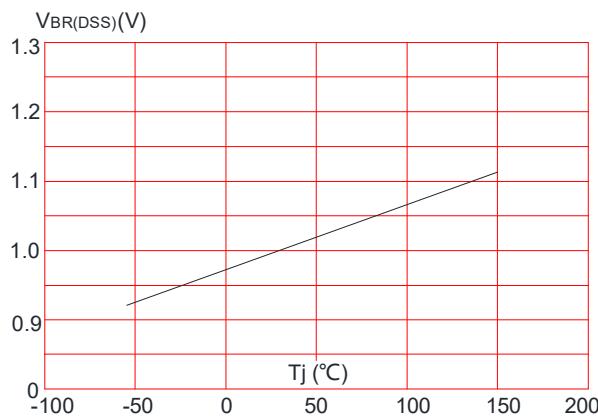
**Figure 4:** Body Diode Characteristics



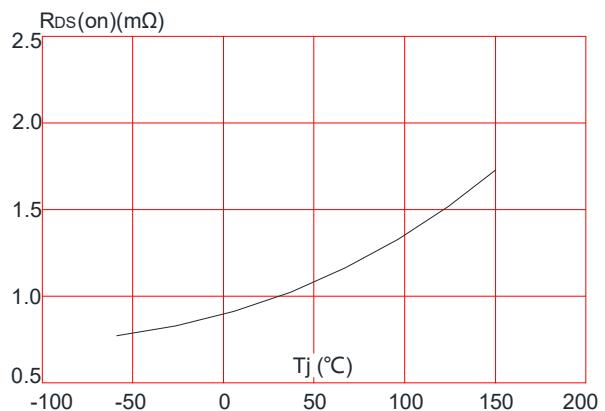
**Figure 6:** Capacitance Characteristics



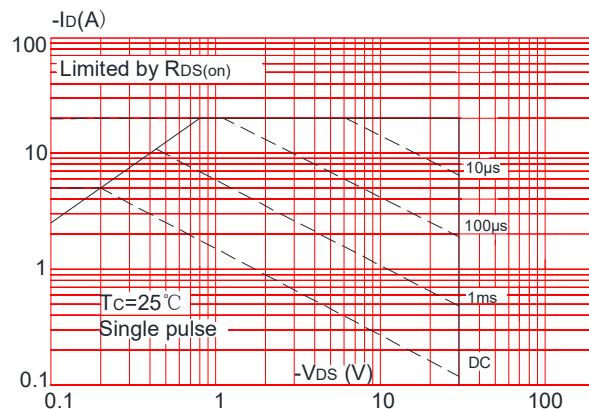
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



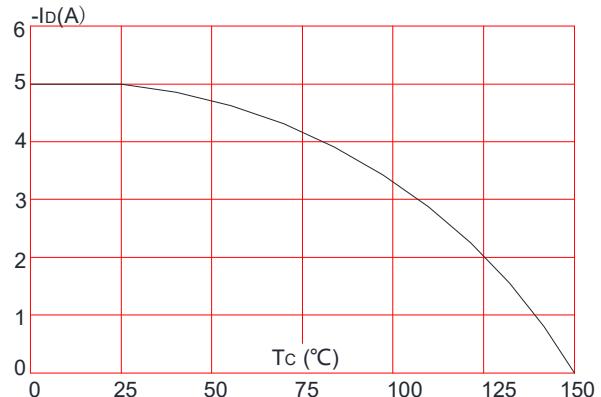
**Figure 8:** Normalized on Resistance vs. Junction Temperature



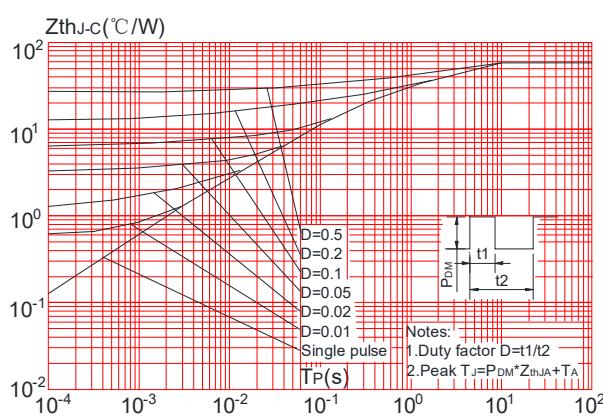
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature

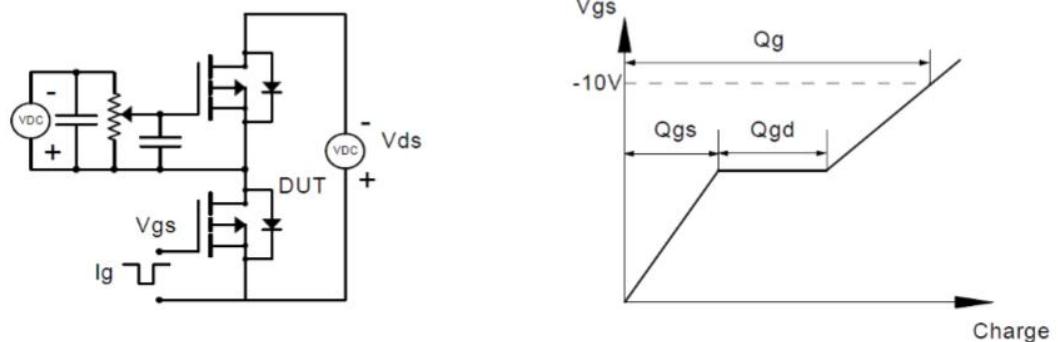


**Figure 11:** Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

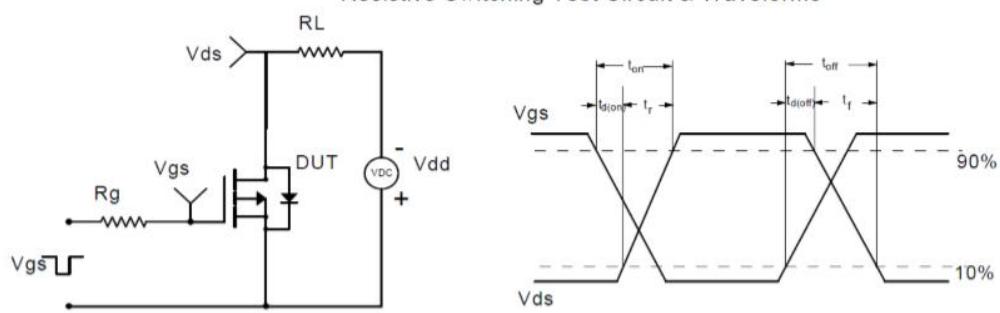


## Test Circuit-P

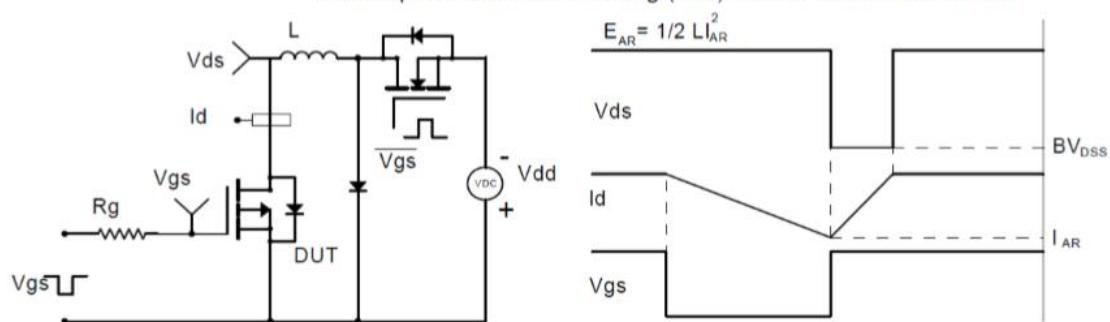
Gate Charge Test Circuit & Waveform



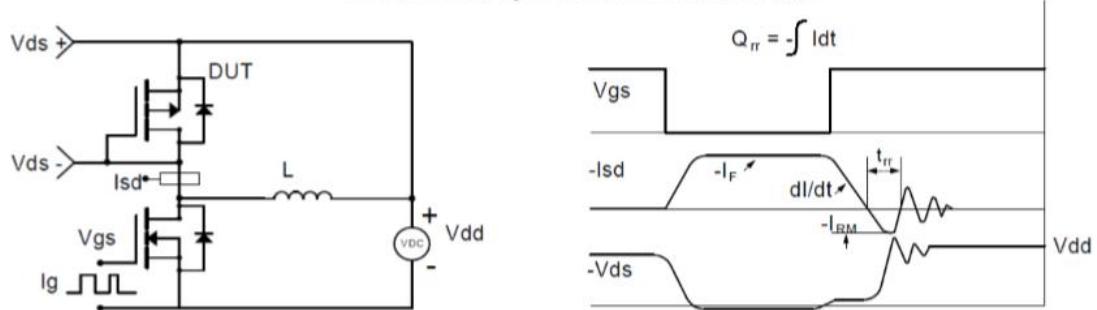
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

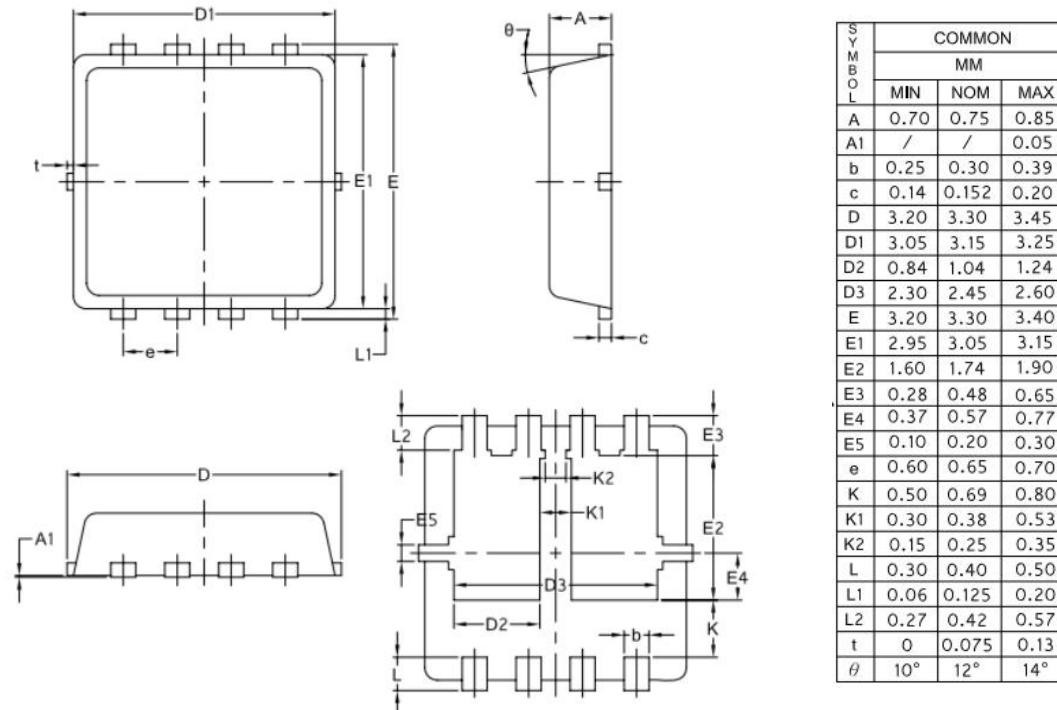


Diode Recovery Test Circuit & Waveforms

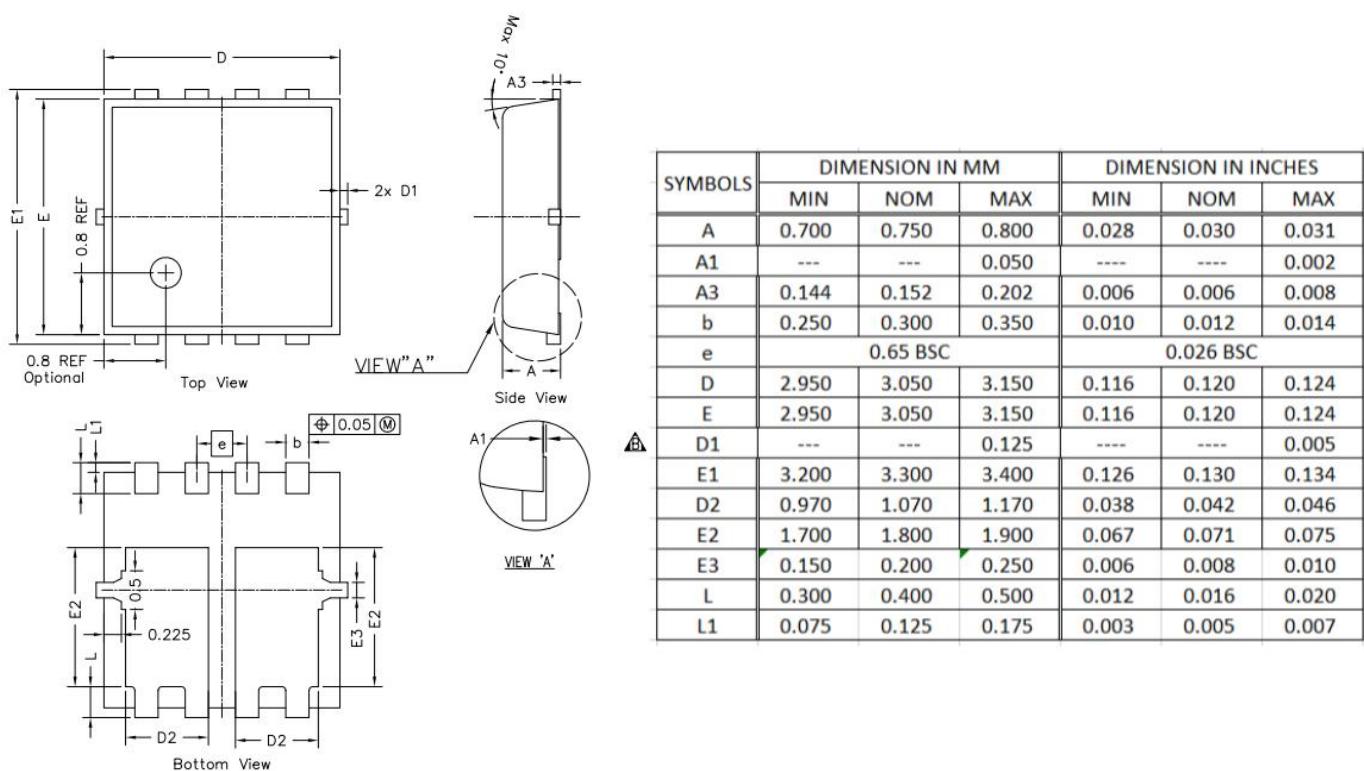




## Package Mechanical Data-PDFN3x3-8L-D-Type A



## Package Mechanical Data-PDFN3x3-8L-D-Type B





**JMTQ250C03D**

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